## **Dielectric characteristics**

## **Class II Dielectrics**

Capacitors of this type have a dielectric constant range of 1000-4000 and also have a non-linear temperature characteristic which exhibits a dielectric constant variation of less than  $\pm 15\%$  (2R1) from its room temperature value, over the specified temperature range. Generally used for by-passing (decoupling), coupling, filtering, frequency discrimination, DC blocking and voltage transient suppression with greater volumetric efficiency than Class I units, whilst maintaining stability within defined limits.



Capacitance and dissipation factor are affected by:-

a) Time (Ageing)

- b) Voltage (AC or DC)
- c) Frequency

Class II Dielectrics									
X5R	X7R			X8R	Class II High Temperature				
Stable	Stable			Stable	Stable				
-	2C1	2R1	2X1	-	-	-	IECQ-CECC	Dielectric	
X5R	-	X7R	-	X8R	-	-	EIA	classifications	
-	BZ	-	BX	-	-	-	MIL		
-	-	-	-	-	-	-	DLI		
BW	-	В	Х	S	G	E, RE	Novacap	Ordering code	
Р	R	Х	В	Ν	-	Х	Syfer		
-	-	Х	-	-	-	-	Voltronics		
-55°C to +85°C		-55°C to +125°C		-55°C to +150°C	-55°C to +160°C	-55°C to +200°C		Rated temperature range	
±15%	±15%	±15%	±15%	±15%	+15 -40%	+15 -65%	No DC voltage applied	Maximum capacitance	
-	+15 -45%	-	+15 -25%	-	-	-	Rated DC voltage applied	temperature range	
≤ 0.025 Typical*	>25V ≤0.025 ≤25V ≤0.035			<u>≺</u> 0.025	<u>≺</u> 0.025			Tangent of loss angle (tan $\delta$ )	
100GΩ or 1000s (whichever is the least)							Time constant (Ri x Cr)	Insulation resistance (Ri)	
±5%, ±10%, ±20%								Capacitance Tolerance	
2.5 times	2.5 times			2.5 times	2.5 times		<u>≤</u> 200V	Dielectric strength Voltage applied for 5 seconds. Charging current limited to 50mA maximum.	
	Rated voltage + 250V				Rated voltage + 250V		>200V to <500V		
	1.5 times				1.5 times		500V to <1kV		
	1.2 times				1.2 times		≥1kV		
55/85/56	55/125/56			55/150/56	-		Chip	Climatic category (IEC)	
-	55/125/21			-	-		Dipped		
-	55/125/56			-	-		Discoidal		
5% Typical			<2% per t	me decade				Ageing characteristic (Typical)	
-	QC-32100	-	-	-	QC-32100	-	Syfer Chip	Approvals	
Refer to page 34	4 for details of Dis	sipation Factor.							