

<b>1210</b>	<b>Y</b>	<b>100</b>	<b>0103</b>	<b>K</b>	<b>X</b>	<b>T</b>	<b>---</b>
Chip Size	Termination	Voltage	Capacitance in picofarads (pF)	Capacitance Tolerance	Dielectric	Packaging	Suffix code

### Case Code

0402
0603
0805
1206
1210
1808
1812
1825
2220
2225
3640
5550
8060

### Termination Codes

<b>A</b>	Nickel barrier	90/10% tin/lead
<b>F</b>	Palladium Silver*	
<b>H</b>	FlexiCap™/Nickel Barrier	90/10% tin/lead
<b>J</b>	Nickel Barrier*	100% tin
<b>Y</b>	FlexiCap™/Nickel Barrier*	100% tin
<b>2</b>	Copper Barrier* (Non Mag)	100% tin
<b>3</b>	FlexiCap™/Copper Barrier* (Non Mag)	100% tin
<b>4</b>	Copper Barrier (Non Mag)	90/10% tin/lead
<b>5</b>	FlexiCap™/Copper Barrier (Non Mag)	90/10% tin/lead

\*Indicates RoHS terminations

### Voltage Code

Code	Value	Code	Value	Code	Value
<b>010</b>	10Vdc	<b>1K0</b>	1kVdc	<b>A25</b>	250Vac
<b>016</b>	16Vdc	<b>1K2</b>	1.2kVdc		
<b>025</b>	25Vdc	<b>1K5</b>	1.5kVdc		
<b>050</b>	50Vdc	<b>2K0</b>	2kVdc		
<b>063</b>	63Vdc	<b>2K5</b>	2.5kVdc		
<b>100</b>	100Vdc	<b>3K0</b>	3kVdc		
<b>200</b>	200Vdc	<b>4K0</b>	4kVdc		
<b>250</b>	250Vdc	<b>5K0</b>	5kVdc		
<b>500</b>	500Vdc	<b>6K0</b>	6kVdc		
<b>630</b>	630Vdc	<b>8K0</b>	8kVdc		
		<b>10K</b>	10kVdc		
		<b>12K</b>	12kVdc		

### Capacitance Tolerance Codes

Code	Tolerance	
<b>H</b>	±0.05pF	< 4.7pF
<b>H</b>	±0.05pF	Cap. Value < 10pF
<b>B</b>	±0.10pF	
<b>C</b>	±0.25pF	
<b>D</b>	±0.50pF	Cap. Value ≥ 10pF
<b>F</b>	±1%	
<b>G</b>	±2%	
<b>J</b>	±5%	
<b>K</b>	±10%	
<b>M</b>	±20%	

### Packaging

Code	
<b>T</b>	178mm (7") reel
<b>R</b>	330mm (13") reel
<b>B</b>	Bulk pack - tubs or trays

### Suffix Definitions

Used for specific customer requirements

<b>PXX</b>	Palladium electrodes
<b>LS*</b>	Chip marking *(consult sales office)

### Dielectric Codes

Code	Dielectric	Features
<b>C</b>	COG/NP0 (1B)	Ultra Stable
<b>H</b>	X8G	Ultra Stable/High Q
<b>P</b>	X5R	Stable
<b>X</b>	X7R (2R1)	Stable
<b>J</b>	X7R (2R1)(BME)	Stable
<b>N</b>	X8R	Stable
<b>Q</b>	COG/NP0 (1B)	Ultra Stable/High Q
<b>U</b>	COG/NP0 (1B)	Ultra Stable/Ultra-low ESR
<b>A</b>	COG/NP0 (1B)	AEC -Q200 approved
<b>S</b>	X7R (2R1)(BME)	AEC -Q200 approved
<b>E</b>	X7R (2R1)	AEC -Q200 approved
<b>T</b>	X8R	AEC -Q200 approved
<b>K</b>	COG/NP0 (1B)(BME)	AEC -Q200 approved
<b>F</b>	COG/NP0 (1B)	IECQ-CECC release
<b>D</b>	X7R (2R1)	IECQ-CECC release
<b>R</b>	BZ (2C1)	IECQ-CECC release
<b>B</b>	BX (2X1)	IECQ-CECC release
<b>G</b>	COG/NP0 (1B)(BME)	Ultra Stable

### Capacitance Code

Calculation	Example	Capacitance value
<1.0pF Insert a P for the decimal point as the 1 <sup>st</sup> character.	<b>P300</b>	0.3pF (values in 0.1pF steps)
≥1.0pF & <10pF Insert a P for the decimal point as the 2 <sup>nd</sup> character.	<b>8P20</b>	8.2pF (values are E24 series)
≥10pF 1 <sup>st</sup> digit is 0. 2 <sup>nd</sup> and 3 <sup>rd</sup> digits are significant figures of capacitance value. 4 <sup>th</sup> digit is number of zeros.	<b>0101</b>	100pF (values are E24 series)

