Bend Testing

Methods and International Specifications

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**Introduction**

Due to its brittle nature, multilayer ceramic capacitors are more prone to excesses of mechanical stress than other components used in surface mounting. One of the most common causes of capacitor failures is directly attributable to the bending of the printed circuit board (PCB) after solder attachment. Excessive bending will create mechanical stress within the ceramic capacitor that, if sufficient, can result in mechanical cracks.

The purpose of this report is to provide details regarding:

- International Specifications that define bend test methods and acceptability.
- Methods employed by Syfer to measure the mechanical performance of the termination material.
- The shape of cracks created by PCB bending - mechanical stress.

**International Requirements/Specifications**

The international requirement for bend testing is referred to in several different specifications.


2. IEC 60068-2-21: 2006 Environmental testing: Test U: Robustness of Terminations and Integral Mounting Devices. Section 8 test Ue specifies the test required to assess the mechanical robustness of surface mounting device terminations when mounted on a substrate. Test Ue1 specifies the substrate bend test.

   The purpose of test Ue1 is to verify that the capacitors can withstand bending loads that are likely to be applied during normal assembly or handling operations.

   IEC 60068-2-21 refers to requirements such as deflection and acceptance criteria as being included in the "relevant specification". Syfer maintains IECQ CECC (International Electrotechnical Commission Quality certification programme- CENELEC Electronic Components Committee) product approval and the "relevant specification" is QC 32100-A001:2007.

3. QC 32100-A001:2007 Table 2 – Periodic Tests defines board flex minimum requirements as:
   - COG: All types, X7R: Y and H only (Flexicap™)
     - 3mm deflection Class I
     - 2mm deflection Class II
     - X7R (non–Flexicap™ termination) 1mm deflection

4. AEC-Q200-005, Board Flex / Terminal Bond Strength Test.
   Minimum requirements stated in table 2 stress test reference 21: 2mm (min) for all except 3mm for Class I.
Capacitor Bend Tests Conducted on Syfer Product

Currently there are 2 methods employed by Syfer to measure the mechanical performance of capacitor termination when mounted on a substrate:

1. External Test Laboratory
   To maintain IECQ-CECC product approval (certified by BSI "British Standards Institute") Syfer issues capacitor samples to an external test laboratory for a variety of tests to be conducted in accordance with IECQ CECC requirements. The external test laboratory is not part of Syfer and has full traceability to International Reference Standards.

   Syfer has maintained IECQ-CECC product approval for >20 years.

2. Syfer Bend Tests
   In addition to the external test laboratory Syfer also conducts bend tests. Samples of capacitors are mounted onto FR4 Test PCBs using 62/36/2 Sn/Pb/Ag solder and subjected to bend testing in accordance with IECQ CECC or AEC –Q200-005 (depending on termination and dielectric types).

Example of FR4 Test PCB Used
Capacitor Placement Method

Syfer’s Bend Test Facility

Fig 1. Bend Test Method
A minimum of 10 Test PCBs (depending on test requirements) are used for each bend test. Each PCB is mounted with one capacitor and deflected automatically until the capacitor breaks. The software analyses the change in capacitance measured by the Agilent 4288A capacitance meter. As soon as the capacitance change is greater than 10% the bend is recorded in mm.

The results of the test are saved to the Syfer network but also can be communicated as a printed document as below.

**Bend Test Data:**

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<th>Sample No.</th>
<th>1% cap. change</th>
<th>10% cap. change</th>
<th>Sample No.</th>
<th>1% cap. change</th>
<th>10% cap. change</th>
<th>Sample No.</th>
<th>1% cap. change</th>
<th>10% cap. change</th>
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</table>

**Extrapolated summary statistics for 10% cap. change:**

- **Mean:** Mean - 3 std. devn.
- **Max:** >10.000
- **Standard deviation:** Mean + 3 std. devn.
- **Min:**

**Comment:**

ROUTINE BEND TEST

Document shows the results for Flexicap™ terminated components
Bend Test Performance Summary

**C0G (NP0) Performance**

![C0G Sintered Termination Performance Chart](image1)

![C0G FlexiCap Termination Performance Chart](image2)

**X7R Performance**

The bend test summary provides a comparison between component case sizes in the following groups:

- C0G (NP0) dielectric material with sintered termination material.
- C0G (NP0) dielectric material with FlexiCap™ termination material.
- X7R dielectric material with sintered termination material.
- X7R dielectric material with FlexiCap™ termination material.

The bend tests conducted confirm that the FlexiCap™ termination withstands greater mechanical strain when compared with sintered termination materials.
**Mechanical Crack Shape**

By conducting extensive bend testing capacitor manufacturers including Syfer have demonstrated that mechanical stress applied by bending the PCB results in a distinctive type of crack within the capacitor.

![Mechanical Crack Diagram](image)

During Syfer’s investigation into mechanical cracking over 15000 capacitors were subjected to bend testing.

Example of capacitors issued by customers to Syfer for failure investigation:

![Example Capacitor Image](image)
Summary

- Syfer capacitors pass the International Specifications for bend testing. In addition to routine tests conducted at Syfer an external test laboratory conducts periodic IECQ CECC tests on Syfer product including bend testing.
- The crack created by mechanical stress during PCB bending is a distinctive type of crack.

For further information regarding:

  a) Potential causes for mechanical cracking refer to Syfer application note “Mechanical Cracking” application note reference AN0005.

  b) Flexicap™. Refer to “Flexicap™ Termination” application note reference AN0001.

  c) AEC-Q200. Refer to “AEC-Q200 Stress Test Qualification for Passive Components” application note reference AN0009.

Information is also available on Syfer’s web site www.knowlescapacitors.com/syfer